DOCKET NO.: 4248

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE MATTER OF THE NEW APPLICATION FOR PATENT

OF: Guenther BERGMANN et al.

USSN: TO BE ASSIGNED

FILED: November 28, 2001

FOR: Method for Tuning a PLL Circuit

BOX NEW PAT APP
U. S. PATENT AND TRADEMARK OFFICE
P. O. BOX 2327
ARLINGTON, VA 22202-0327

November 28, 2001

## SECOND VOLUNTARY AMENDMENT

Dear Sir:

After calculating the filing fee and granting a filing date, but before the first examination, please amend the above identified application as follows.

## In the Claims:

Claims 1 to 7 are maintained unchanged.

Claims **8** to **10** have previously been cancelled by applicant's First Preliminary Amendment.

Please enter new claims 11 to 13 as follows.

11. (new) Method according to claim 1, wherein the phase detector (PD) generates a control signal (LD) to indicate that the output frequency (OF) matches the desired frequency (FR).

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- 1 12. (new) PLL circuit arrangement for implementing the method according to claim 1, comprising
- a phase detector (PD) which is linked to a loop filter (SF),
- a voltage-controlled oscillator (VCO) which is wired to the loop filter (SF),
- at least one control unit (ST) which is linked to the loop filter (SF) and the phase detector (PD) in order to monitor the control voltage (VC) of the oscillator (VCO), and
- a current source (IQ) and a current sink (IS) which are linked to the loop filter (SF) by means of a switching element (E).
  - 13. (new) PLL circuit arrangement according to claim 12, wherein the control unit (ST) for comparing the control voltage with the two threshold values has at least one comparator and has at least one memory unit for storing the operating state.

## **REMARKS:**

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Amendment. No new matter is introduced by the present amendment. New claims 11 to 13 are based directly in sequence on the original literally translated claims 8 to 10 except for omitting the multiple dependencies.

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